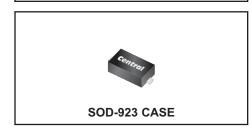


SURFACE MOUNT SILICON TRANSIENT VOLTAGE SUPPRESSORS



APPLICATIONS:

- PDAs
- Memory Card Ports
- Mobile Phones
- Instrumentation

MAXIMUM RATINGS: (T_A=25°C)

Peak Power Dissipation (8x20µs) P_{PK} ESD Voltage (IEC 61000-4-2, Air) VESD ESD Voltage (IEC 61000-4-2, Contact) VESD Operating and Storage Junction Temperature TJ, Tstg

ELECTRICAL CHARACTERISTICS: (T_A=25°C) V_F=0.9V MAX @ I_F=10mA (for all types)

Туре	Maximum Reverse Stand-off Voltage	Minimum Breakdown Voltage	Test Current	Maximum Reverse Leakage Current	Maximum Clamping Voltage	Peak Pulse Current	Typical Capacitance @ 0V Bias
	VRWM	V _{BR} @ I _T	г	I _R @ V _{RWM}	V _C @ I _{PP}	IPP	С
	V	v	mA	μA	V	А	pF
CMATVS3V3	3.3	5.0	1.0	2.5	11	7.0	45
CMATVS5V0	5.0	6.2	1.0	1.0	12.3	7.0	40



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UNITS

W

kV

kV °C

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMATVS3V3 Series types are Low Leakage, Fast Response TVSs packaged in a space saving SOD-923 surface mount package. These devices are designed to protect sensitive equipment against ESD damage.

MARKING CODES: CMATVS3V3: E CMATVS5V0: F

FEATURES:

SYMBOL

• Small, 1.0 x 0.6 x 0.4mm, SOD-923 Surface Mount Package

80

30

30

-55 to +150

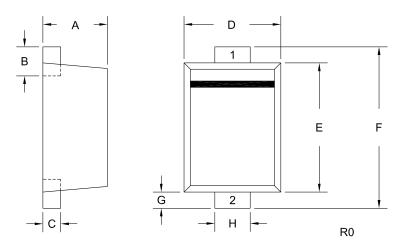
Low Leakage Current

R5 (2-April 2015)



CMATVS3V3 CMATVS5V0

SURFACE MOUNT SILICON TRANSIENT VOLTAGE SUPPRESSORS



SOD-923 CASE - MECHANICAL OUTLINE

DIMENSIONS									
	INC	HES	MILLIMETERS						
SYMBOL	MIN	MAX	MIN	MAX					
А	0.015	0.016	0.39	0.41					
В	0.004	0.010	0.10	0.26					
С	0.003	0.006	0.08	0.14					
D	0.022	0.026	0.55	0.65					
E	0.030	0.033	0.75	0.85					
F	0.035	0.043	0.90	1.10					
G	0.002	0.006	0.05	0.15					
Н	0.007	0.011	0.17	0.27					

SOD-923 (REV: R0)

LEAD CODE: 1) Cathode 2) Anode

MARKING CODES: CMATVS3V3: E CMATVS5V0: F

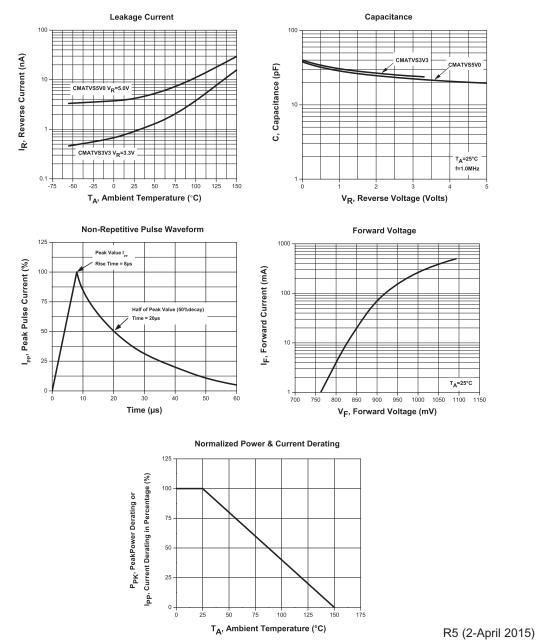
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CMATVS3V3 CMATVS5V0

SURFACE MOUNT SILICON TRANSIENT VOLTAGE SUPPRESSORS



TYPICAL ELECTRICAL CHARACTERISTICS

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CMATVS3V3 CMATVS5V0



SURFACE MOUNT SILICON TRANSIENT VOLTAGE SUPPRESSORS

SERVICES

- · Bonded Inventory
- Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM™)
- Bare Die Available for Hybrid Applications

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Buyer understands and agrees that the foregoing liability limitations are essential elements of a purchase order or contract and that in the absence of such limitations, the material and economic terms of the purchase order or contract would be substantially different.

R5 (2-April 2015)

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